## Colossal dielectric permittivity in (AI + Nb) co-doped rutile SnO<sub>2</sub> ceramics with low loss at room temperature

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## Colossal dielectric permittivity in (AI + Nb) co-doped rutile SnO<sub>2</sub> ceramics with low loss at room temperature

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The exploration of colossal dielectric permittivity (CP) materials with low dielectric loss in a wide range of frequencies/temperatures continues to attract considerable interest. In this paper, we report CP in (Al + Nb) co-doped rutile SnO<sub>2</sub> ceramics with a low dielectric loss at room temperature. Al<sub>0.02</sub>Nb<sub>0.05</sub>Sn<sub>0.93</sub>O<sub>2</sub> and Al<sub>0.03</sub>Nb<sub>0.05</sub>Sn<sub>0.92</sub>O<sub>2</sub> ceramics exhibit high relative dielectric permittivities (above 10<sup>3</sup>) and low dielectric losses (0.015 < tan  $\delta$  < 0.1) in a wide range of frequencies and at temperatures from 140 to 400 K. Al doping can effectively modulate the dielectric behavior by increasing the grain and grain boundary resistances. The large differences in the resistance and conductive activation energy of the grains and grain boundaries suggest that the CP in co-doped SnO<sub>2</sub> ceramics can be attributed to the internal barrier layer capacitor effect. *Published by AIP Publishing*. [http://dx.doi.org/10.1063/1.4964121]

The compounds with a colossal dielectric permittivity  $(CP > 10^3)$  have attracted a lot of attention because of their applications in high-energy density storage and microelectronics as a result of the continually increasing demands of microelectronics applications.<sup>1-4</sup> Ferroelectric CP materials, such as BaTiO<sub>3</sub>,<sup>5,6</sup> Bi<sub>0.5</sub>Na<sub>0.5</sub>TiO<sub>3</sub>,<sup>7</sup> (K,Na)NbO<sub>3</sub>,<sup>8</sup> and  $(1-x)[Pb(Mg_{1/3}Nb_{2/3})O_3-x[PbTiO_3] (PMN-PT),^{9,10}$  have been widely studied; however, the CP can only be achieved in a small temperature range around the Curie temperature, which limits their application. CP has been reported across a wide range of temperatures in the traditional nonferroelectric materials, such as  $CaCu_3Ti_4O_{12}$  (CCTO),<sup>2,11</sup> doped NiO,<sup>12</sup> La<sub>2x</sub>Sr<sub>x</sub>NiO<sub>4</sub>,<sup>13,14</sup> and ZnO;<sup>15,16</sup> however, the dielectric loss in these materials is too high (>0.1) to be applied. Although doped CCTO<sup>17</sup> and doped Y<sub>2/3</sub>Cu<sub>3</sub>Ti<sub>4</sub>O<sub>12</sub> (Ref. 18) show CP with low dielectric loss, the low dielectric loss can only be achieved in a narrow frequency range. Recently, Hu *et al.* found that In + Nb co-doped rutile TiO<sub>2</sub> ceramics exhibit CP with low dielectric loss (~0.05) over a very broad range of temperatures/frequencies,<sup>19</sup> but the low dielectric loss strongly depends on the synthesis conditions.<sup>20–23</sup> Therefore, new CP materials with low dielectric loss over a wide range of temperatures and frequencies are still being investigated.

Doped SnO<sub>2</sub> was investigated as a kind of varistor with insulating grain boundaries and semi-conducting grains, similar to CCTO and doped TiO<sub>2</sub> ceramics in which CP was discovered.<sup>24–27</sup> CP was also observed in doped SnO<sub>2</sub> ceramics; however, CP can only be observed at low frequencies in Zn+Co co-doped SnO<sub>2</sub> ceramics.<sup>28</sup> Nb doping can generate n-type carriers that can result in CP and high dielectric loss in tetravalent oxides. Trivalent cation doping can be

considered acceptor doping and can capture the electrons introduced by Nb doping in tetravalent oxides. As a result, the concentration of carriers can be acutely tuned by adjusting the ratio between Nb and trivalent cations, which will result in different grain and grain boundary resistances for co-doped tetravalent oxides.<sup>29,30</sup> Therefore co-doped SnO<sub>2</sub> may be a type of CP material with low dielectric loss.

In this paper, we investigated the dielectric properties of Al + Nb co-doped rutile SnO<sub>2</sub> ceramics. CP ( $\sim 10^3$ ) with a low dielectric loss (0.02–0.05) was observed in Al<sub>0.02</sub>Nb<sub>0.05</sub>Sn<sub>0.93</sub>O<sub>2</sub> and Al<sub>0.03</sub>Nb<sub>0.05</sub>Sn<sub>0.92</sub>O<sub>2</sub> ceramics across a wide range of frequencies and temperatures. The large difference in resistance and conductive activation energy between grains and grain boundaries indicates that the internal barrier layer capacitor (IBLC) effect may be responsible for the CP.

The Al+Nb co-doped  $SnO_2$  samples were synthesized using the standard conventional solid-state reaction process, where rutile SnO<sub>2</sub> (99.9%; Alfa Aesar), Nb<sub>2</sub>O<sub>5</sub> (99.9%; Alfa Aesar), and Al<sub>2</sub>O<sub>3</sub> (99.9%; Alfa Aesar) powders were used as precursors. The powders were mixed and then pressed into a disk. The temperature was increased at a rate of 2 °C/min, and the disk was sintered at 1500 °C in air for 20 h. The X-ray diffraction (XRD) patterns of the Al<sub>x</sub>Nb<sub>0.05</sub>Sn<sub>0.95-x</sub>O<sub>2</sub> ceramics acquired using a PANalytical X'PERT PRO powder diffractometer indicated that all ceramics were in the pure rutile phase, as shown in Fig. S1 (supplementary material). The microstructure was investigated with the scanning electron microscopy (UItra55, Carl ZeissNTS GmbH). Figs. S2 and S3 (supplementary material) show the scanning electron microscope (SEM) images and the energy dispersive X-ray spectroscopy (EDX) results, respectively, for  $Al_x Nb_{0.05} Sn_{0.05-x} O_2$ (x = 0.01, 0.03, 0.05, and 0.07) ceramic surfaces. The smallest grain size ( $<1 \mu m$ ) was obtained for the sample with x = 0.01, and the grain size increased with Al doping. The dielectric properties and the impedance spectroscopy were measured

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using Agilent 4980A in the Physical Property Measurement System and a stove at low and high temperature, respectively. The room-temperature (RT) dielectric properties were measured at high frequencies using the Agilent 4292A precision impedance analyzer.

Fig. 1 shows the dielectric permittivity and loss of Al<sub>x</sub>Nb<sub>0.05</sub>Sn<sub>0.95-x</sub>O<sub>2</sub> ceramics at room temperature. CP  $(3 \times 10^3)$  was observed along with high dielectric loss in Nb-doped SnO<sub>2</sub> ceramics. Al doping effectively reduces the dielectric loss, with the lowest dielectric loss value observed for Al doping levels of 2% and 3%. Further increases in the Al doping concentration (>4%) led to an observed increase in the dielectric loss and a plateau in the dielectric permittivity. The frequency at which the dielectric permittivity plateau appears gradually decreased with increasing Al doping concentration. The dielectric permittivity and loss data at 1 kHz, 10 kHz, 100 kHz, and 1 MHz at 300 K and 350 K are shown in Fig. S4 (supplementary material).<sup>30</sup> The frequency independence of the CP and low loss were observed in  $Al_xNb_{0.05}Sn_{0.95-x}O_2$  (x = 0.01, 0.02, and 0.03) ceramics. In the frequency range from 1 kHz to 1 MHz, the dielectric losses were approximately 0.03 and 0.02 in Al<sub>0.03</sub> Nb<sub>0.05</sub>Sn<sub>0.92</sub>O<sub>2</sub> and Al<sub>0.02</sub>Nb<sub>0.05</sub>Sn<sub>0.93</sub>O<sub>2</sub> ceramics, respectively, which are much lower than that observed in CCTO,<sup>17</sup> co-doped TiO<sub>2</sub>,<sup>21,30</sup> and doped NiO.<sup>12</sup> Although lower dielectric loss values have been reported in doped CCTO<sup>17</sup> and  $Y_{2/3}Cu_3Ti_4O_{12}$ ,<sup>18</sup> these low dielectric loss values are only observed in a narrow frequency range, just like for  $Al_{0.01}Nb_{0.05}Sn_{0.96}O_2$  ceramics.

The temperature dependence of the dielectric permittivity and dielectric loss of  $Al_{0.02}Nb_{0.05}Ti_{0.93}O_2$  at various frequencies is shown in Fig. 2. CP (>10<sup>3</sup>) was observed in a broad range of temperatures from 100 K to 450 K. The dielectric permittivity increased dramatically, from 10<sup>1</sup> to 10<sup>3</sup>, with increasing temperature to 100 K, after which another dielectric permittivity plateau appeared as the temperature increased to 450 K. The temperature dependence of the dielectric permittivity in the temperature range from 100 K to 450 K is very weak, which is quite similar to the behavior observed in CCTO<sup>31</sup> and co-doped TiO<sub>2</sub> ceramics.<sup>30</sup> Interestingly, a low dielectric loss was observed for temperatures from 140 K to 400 K. The relaxation time  $\tau$  can be calculated from the imaginary part of the dielectric



FIG. 1. Dielectric permittivity and loss of  $Al_xNb_{0.05}Sn_{0.95-x}O_2$  ceramics at room temperature.



FIG. 2. Dielectric permittivity (a) and loss (b) of  $Al_{0.02}Nb_{0.05}Sn_{0.93}O_2$  ceramics as a function of temperature. The inset of (a) shows the relaxation time at low temperature.

permittivity using  $2\pi\tau f_p = 1$ , where  $f_p$  is the frequency at which the imaginary part of the dielectric permittivity is a maximum. The temperature dependence of  $\tau$  can be described by the following formula:<sup>2</sup>

$$\tau = \tau_0 \exp\left(\frac{E_{a1}}{\mathbf{k}_{\rm B}T}\right),\tag{1}$$

where  $\tau_0$  is a constant,  $E_{a1}$  is the conductive activation energy of the grain,  $k_B$  is the Boltzmann constant, and *T* is the temperature. The temperature dependence of  $\tau$  is shown in the inset of Fig. 2. The conductive activation energy of the grain in Al<sub>0.02</sub>Nb<sub>0.05</sub>Sn<sub>0.93</sub>O<sub>2</sub> ceramics was calculated to be 0.074 eV using Eq. (1), which is on the same order of magnitude as the grain conductive activation energy in CCTO<sup>11</sup> and co-doped TiO<sub>2</sub><sup>30</sup> ceramics.

The capacitance determined from the fit to the impedance spectra of  $Al_{0.02}Nb_{0.05}Sn_{0.93}O_2$  ceramics at high temperatures was 0.2 nF cm, as shown in Fig. 3. The relative



FIG. 3. Complex plane plot of impedance of  $Al_{0.02}Nb_{0.05}Sn_{0.93}O_2$  ceramics at high temperature. The inset shows the temperature dependence of grain boundary resistance.



FIG. 4. (a) Complex plane plot of impedance of  $Al_xNb_{0.05}Sn_{0.95-x}O_2$  ceramics at room temperature. (b) The real part of the resistivity of  $Al_xNb_{0.05}Sn_{0.95-x}O_2$  ceramics as a function of frequency.

intrinsic dielectric permittivity of CCTO is 80, and the typical capacitance of the grain boundary is  $1-10 \text{ nF cm.}^{11}$  The relative dielectric permittivity of pure rutile SnO<sub>2</sub> is only 12, which is much smaller than that of CCTO. These results suggest that the arc shown in Fig. 3 can be attributed to the grain boundary response of Al + Nb co-doped SnO<sub>2</sub>. The grain boundary resistance decreased with increasing temperature, and their relationship can be described by the following formula:<sup>11</sup>

$$R = \mathbf{R}_0 \exp\left(\frac{E_{a2}}{\mathbf{k}_{\mathrm{B}}T}\right),\tag{2}$$

where *R* is the resistance,  $R_0$  is a constant, and  $E_{a2}$  is the conductive activation energy of the grain boundary. The conductive activation energy of the grain boundary was calculated to be 0.36 eV, which is consistent with those of co-doped TiO<sub>2</sub> (Ref. 30) and CCTO<sup>11</sup> CP ceramics. In the oxide semiconductors, oxygen vacancies are easily introduced into the grain instead of the grain boundary in sintering processes at high temperature. The resistivity can be effectively improved by suppressing the production of oxygen vacancies because a small amount of oxygen loss generated during high-temperature sintering, in the order of 100 ppm, could lead to an n-type semiconductivity.<sup>32</sup> Therefore, the  $E_a$  of the grain can be attributed to the interaction between doped Nb and

oxygen vacancies, and the  $E_a$  of the grain boundary is a typical band gap for a doped semiconductor,<sup>21,33</sup> which can be attributed to the impurity energy level introduced by doped Nb, which is considered donor doping. The huge difference in conductive activation energy between grains and grain boundaries indicates that the internal barrier layer capacitor (IBLC) effect may be responsible for the CP in Al + Nb codoped SnO<sub>2</sub> ceramics. In addition, the second dielectric plateau in Fig. 2 at the high-temperature range can be attributed to the surface barrier layer capacitor (SBLC) effect, as observed in In + Nb co-doped TiO<sub>2</sub> samples.<sup>30</sup>

We investigated the influence of Al-Nb co-doping on the resistance of the grains and grain boundaries to explain the dielectric properties, as the resistance of the grains and grain boundaries plays an important role in the dielectric behaviors in CP ceramics with an IBLC effect.<sup>21,29</sup> The valence state configurations of Al, Sn, and Nb in Al+Nb co-doped  $SnO_2$  are +3, +4, and +5, respectively. Therefore, Nb doping in SnO<sub>2</sub> can be considered donor doping and introduces many n-type carriers. Meanwhile, Al doping in SnO<sub>2</sub> can be considered acceptor doping and can capture the electrons introduced by Nb doping, increasing the resistance of the grain and grain boundary. Therefore, the concentration of carriers and the resistance of the grains and grain boundaries can be acutely tuned by varying the ratio between Nb and Al cations, which will result in CP and low dielectric loss in co-doped SnO<sub>2</sub>.

The complex plane plots of the room temperature (RT) impedance of  $Al_xNb_{0.05}Sn_{0.95-x}O_2$  ceramics (x = 0.01, 0.02, and 0.03) are shown in Fig. 4(a), with the data for x = 0shown in the inset. The grain boundary response arc can be identified clearly, and the calculated resistivity of the grain boundary is shown in Table I. The grain boundary resistivity was too large to be fit using a complex impedance at RT for the samples with  $x \ge 0.04$ , so the complex impedance at higher temperatures, as shown in Fig. S5 (supplementary material), was used to calculate the resistivity of the grain boundary at RT according to Eq. (2) for x = 0.04 ceramics.<sup>30</sup> The increase in grain boundary resistivity with increasing Al doping confirms that the lower dielectric loss in Al + Nb codoped SnO<sub>2</sub> can be attributed to the increasing grain boundary resistance with Al doping.<sup>21</sup> The frequency dependence of the real part of the RT resistivity for Al<sub>x</sub>Nb<sub>0.05</sub>Sn<sub>0.95-x</sub>O<sub>2</sub> ceramics is shown in Fig. 4(b). The conductivity at high frequency is dominated by the grain conductance.<sup>21</sup> With increasing Al doping, grain resistance increases, as shown in Fig. 4(b) and Table I. The large difference between grain resistance and grain boundary resistance indicates that the IBLC effect is responsible for the CP in Al + Nb co-doped SnO<sub>2</sub> ceramics.

The dielectric loss is also strongly affected by the relaxation time. The relaxation time  $\tau$  was determined from the

TABLE I. Grain and grain boundary resistivity of  $Al_xNb_{0.05}Sn_{0.95-x}O_2$  at room temperature.

| x  | 0    | 0.01                 | 0.02                 | 0.03                 | 0.04                 | 0.05    | 0.06    | 0.07    |
|--|------|----------------------|----------------------|----------------------|----------------------|---------|---------|---------|
| $\frac{R_{gb}\left(\Omega\;cm\right)}{R_{g}\left(\Omega\;cm\right)}$ | 6911 | $5.73 \times 10^{6}$ | $9.59 \times 10^{7}$ | $3.06 \times 10^{8}$ | $5.60 	imes 10^{10}$ |         |         |         |
|  | 9.09 | 9.34                 | 16.69                | 41.76                | 165.91               | 1502.11 | 7869.91 | 9543.97 |

grain resistance  $R_g$  and grain boundary capacitance  $C_{gb}$  and can be described by the following formula:

$$\tau = C_{gb} R_g. \tag{3}$$

Furthermore, the relationship between the relaxation time  $\tau$ and frequency at which the peak dielectric loss occurs  $(f_p)$  is  $2\pi\tau f_p = 1$ .  $\tau$  increases with increasing  $R_g$ , which suggests that  $f_p$  gradually shifts to lower frequencies with increasing  $R_{g}$ . A step in the dielectric permittivity is clearly observed when the Al doping content is greater than 0.05, and the frequency where the dielectric permittivity begins to drop decreases with increasing  $R_g$  (as shown in Fig. 1). Therefore, the increasing grain resistance may be responsible for the higher dielectric loss as well as the strong frequency dependence of the dielectric permittivity and loss. To verify this conjecture, the dielectric permittivity and loss were measured at frequencies up to 100 MHz at RT. As shown in Fig. 5, the dielectric permittivity plateau and dielectric loss peak can be observed in the Al<sub>x</sub>Nb<sub>0.05</sub>Sn<sub>0.95-x</sub>O<sub>2</sub> ceramics (x = 0.04, 0.05, and 0.06). A dramatic drop in the dielectric permittivity (from  $10^3$  to  $10^1$ ) and higher dielectric loss were observed across a wide range of frequencies around the loss peak. More importantly, the dielectric loss peak shifted to lower frequencies with increasing Al doping content, which indicates that the relaxation time  $\tau$  increases with increasing grain resistance. The high-frequency experimental data corroborates our conjecture that the increase in the dielectric loss for Al doping content over 3% can be attributed to the larger grain resistance at higher Al-doping levels.

In summary, we have reported CP and low loss in Al + Nb co-doped rutile  $SnO_2$  ceramics across a broad range of frequencies and temperatures. Al + Nb co-doped  $SnO_2$  ceramics exhibit lower dielectric loss than traditional non-ferroelectric CP materials, with an excellent independence of frequency/temperature. The large differences in the



resistance and conductive activation energy between the grain and grain boundary suggest that the IBLC effect is responsible for the CP. Al doping can modulate the dielectric behavior by increasing grain and grain boundary resistances. The increase in grain boundary resistance can effectively reduce dielectric loss; meanwhile, the increasing grain resistance will increase the relaxation time  $\tau$ , resulting in a higher dielectric loss when the Al doping level exceeds 0.04.

## See supplementary material for figures.

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